



ARDUINO MICRO PINS associated functions

The following list is the map of the hardware signals and of their function used in this prototype and is valid with ARDUINO MICRO.

Is possible use other version of ARDUINO board, but must be look for the correct pins position of the corresponding signal.



ARDUINO MICRO PINS associated functions (1)

Name on ARDUINO MICRO		Name on schematic	Associated function on card	
PIN 1	MOSI	Not used		
PIN 2	RXLED/SS	Not used		
PIN 5	RESET	Not used		
PIN 6	GND	GND	Power and signal ground reference	
PIN 10	D5	Not used		
PIN 19	3V3	Not used	Output internal 3,3 V supply of ARDUINO MICRO	
PIN 27	Not connected	Not used		
PIN 28	Not connected	Not used		
PIN 29	+5V	Not used	This voltage is generated by internal linear regulator of ARDUINO MICRO. The 5V and +5VHALL generated by linear regulator present on the card don't must be wired with this pin	
PIN 30	RESET	Not used		
PIN 31	GND	GND	Power and signal ground reference	
PIN 32	VI	Vin	Input 12 V supply for ARDUINO MICRO	
PIN 33	MISO	Not used		
PIN 33	SCK	Not used		



ARDUINO MICRO PINS associated functions (2 continue)

Name on ARDUINO MICRO		Name on schematic	Associated function on card
Signals r			MMUNICATION EXPANSION———————————————————————————————————
PIN 3	D1/TX	D1/TX	Reserved for communication MASTER <>SLAVE Reserved for communication MASTER <>SLAVE
PIN 4	D0/RX	D0/RX	
PIN 7	D2/SDA	TX EN	Reserved for communication MASTER <>SLAVE
PIN 9	D4/PWM8	NET_ALL INPUT	Reserved for communication MASTER <>SLAVE Reserved for communication MASTER <>SLAVE
PIN 13	IO8	ADD_NODE_1	
PIN 15	IO10/PWM16	ADD_NODE_2	Reserved for communication MASTER <>SLAVE Reserved for communication MASTER <>SLAVE
PIN 18	IO13/PWM10	READY	



ARDUINO MICRO PINS associated functions (3 continue)

Name or	ARDUINO MICRO	Name on schematic	Associated function on card
Signals i		POWER FAIL DETECT = ion serial communication	between MASTER and SLAVEs
PIN 8	D3/SCL	PF_INT	Power Fail signal Interrupt INT1 (INPUT). This signal is generated by a circuit connected directly to the terminal pin of the +24 Vdc input. During power down the residual time to execute the function of data save is proportional to the electrolytic capacitor values. The time for each save operation (1 byte) require minimum 3,3 ms. A digital transition from high to low level alarm interrupt routine 0, routine PowerFail()



ARDUINO MICRO PINS associated functions (4 continue)

Name on ARDUINO MICRO		Name on schematic	Associated function on card		
 MOTOR COMMAND SIGNALS					
PIN 11	D6/PWMH	Reverse	Digital command (OUTPUT)to driver coil RL1 (transistor)		
PIN 12	D7	Forward	Digital command (OUTPUT)to driver coil RL2 (transistor)		
PIN 14	IO9/PWM16	PWM 1	Pulse Wave Modulation Motor command signal (OUTPUT).		
PIN 21	AO	CUR_MONITOR	The output frequency is 2 kHz (with modifying on TIMER 1) Analog signal. The voltage level is proportional to rms value of the current flowing into the coil of the motor		



ARDUINO MICRO PINS associated functions (5 continue)

Name or	ARDUINO MICRO	Name on schematic	Associated function on card
		= SENSOR HALL SIGNALs II	NTERFACE ====================================
PIN 16 PIN 17	IO11 IO12	ENC_B ENC_A	Digital state (INPUT) sensor hall 2 (GREEN wire of the CONCENS) Digital state (INPUT) sensor hall 1 (YELLOW wire of the CONCENS)
Name o	n ARDUINO MICRO	O Name on schematic	Associated function on card
Signals res			NSOR INTERFACE ====================================
PIN 20 PIN 22	AREF A1	V_REF_POS ABS_POS	Reference output voltage supply for potentiometer sensor Reserved for analog signal position



ARDUINO MICRO PINS associated functions (6)

Name on	ARDUINO N	IICRO Name on schematic	Associated function on card		
	Digital signals reserved for command operator interface				
PIN 23	A2	OP MODE 0	Forward digital signal (INPUT)		
PIN 24	A3	OP MODE 1	Reverse digital signal (INPUT)		
PIN 25	A4	OP MODE 2	Calibration (Homing) digital signal (INPUT)		
PIN 26	A5	OP_MODE_3	MANUAL/CYCLE (ĽÓCĂL/NETWORK) digital signal (INPUT)		